

BarcoSilex

MPEG2 decoder IP

V1.0

MPEG2 decoder IP core

- o Compliant with ISO/IEC 13818-2 standard (ITU-T H.262)
- o Simple, main and 422 profiles
- o Low, main and high levels
- o Multiple channels
- o Intra and inter frame (I, P and B frames)
- o SD, HD, 2K, 4K resolution
- o Frame rate up to 120 fps for progressive full HD
- o Interlaced and progressive format
- o 4:2:2 and 4:2:0 Chroma formats
- o 8 bits per pixel component
- o 100% CPU offload

Complete solutions

- o Custom design for complete video processing chain
- o Video, serial, networking, PCIe interfaces
- o Multi-port memory controllers (DDR2, DDR3, ...)

Barco Silex portfolio offers flexible, high-end Intellectual Property products. Our Mpeg2 decoder IP core can more than meet the requirements of demanding applications such as **broadcast, production, digital cinema, video conferencing...**

These remarkably flexible IP cores have already been proven in high-end market environments. They deliver real-time decoding of high-resolution and multi-channel video streams such as **2K, 4K, SD, HD...**

The flexible architecture of our latest generation of IP cores provides the highest standards of portability, flexibility, performance and compactness.

- **Portability:** support for all leading-edge FPGAs
- **Flexibility:** easy to integrate interface
- **Performance:** faster than real-time to support more simultaneous channels, larger images or higher frame-rate
- **Compactness:** cost-effective, low resource count

Deliveries include RTL or netlist, simulation testbench, implementation example and bit accurate C model.

Barco Silex is a leader in contract engineering services, custom hardware and software development, as well as Intellectual Property (IP) cores.

Barco Silex' s history as a custom electronic design house (ASIC, FPGA, DSP, board) specialized in video coding, cryptography, security and memory controllers goes back to 1991. Barco Silex offers the best guarantee for continuous support throughout the complete lifecycle of products.

